

Notice of References Cited	Application/Control No. 10/602,581	Applicant(s)/Patent Under Reexamination ADAMS ET AL.	
	Examiner Thomas J. Cleary	Art Unit 2111	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,661,427	12-2003	MacInnis et al.	345/660
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"PALMPAK Fast SOC Platform". PALM-DP-1050. Palmchip Corporation. 31-Jan. 2000. Document Number DP-1050-0.5.
	V	"AcurX Configurable SoC Platform". PALM-DP-2000. Palmchip Corporation. Oct. 2002. Document Number DP-2000B-1.0.
	W	Adams, Lyle. "Overview of the CoreFrame Architecture" CoreFrame View White Paper. Palmchip Corporation.
	X	Cordan, Bill. "An Efficient Bus Architecture for System-on-Chip Design". IEEE 1999 Custom Integrated Circuits Conference. Institute of Electrical and Electronics Engineers. 1999. Pages 623-626. Document Number 0-7803-5443-5/99.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.